

The listing of the claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1 (Currently Amended): A method for suppressing latch-ups occurring in a circuit, wherein, in a current-limited supply voltage, an undervoltage is detected, the supply voltage is switched off following the detection of a latch-up, and charge located in the circuit is reduced, ~~characterized in that~~ wherein the charge existing in the circuit is reduced by a short-circuiting switch and, during restoration of the supply voltage, an undervoltage detection is suppressed for a short time.

Claim 2 (Currently Amended): A system for performing the methods according to claim 1 for protection of radiation-sensitive active circuit components of an electronic circuit, ~~characterized in that~~ wherein the electronic circuit is subdivided into groups of active circuit components with substantially the same current consumption in a predefined area, and at least one of these groups of active circuit components

with substantially the same current consumption in a predefined area has a protective circuit (SSG) assigned to it.

Claim 3 (Currently Amended): The system according to claim 2, ~~characterized in that~~ wherein the protective circuit comprises a voltage controller (SR) adapted to be switched off and allowing for adjustment of the current limitation, a actuator (SG), a comparator (COMP) for detection of undervoltage, two monoflops (MFz and MFSK), a short-circuiting switch (KS) with current limitation and, at the output, at least one capacitor (Cout).

Claim 4 (Currently Amended): The system according to claim 2 or 3, ~~characterized in that~~ wherein a unit for current detection is arranged upstream of a unit for voltage control to thereby avoid an influence of the input current on the output voltage.

Claim 5 (Currently Amended): The system according to any ~~one of claims 2 to 4~~ claim 2, ~~characterized in that~~ wherein, for switching off a plurality or all of the groups of active circuit components having respectively one protective circuit (SSG)

assigned thereto, a signaling line (SIL) and a control line (STL) are provided which connect the protective circuits (SSG) of the groups of active circuit components on the output side and which themselves are connected to a central monoflop (MFz), so that, upon detection of a latch-up in one of the protective circuits (SSG), the central monoflop (MFz) is started via the signaling line (SIL) whereupon, via the control line (STL), all voltage controllers (SR) are switched off and all shortcircuiting switches (KS) of the protective circuits (SSG) are activated and, after lapse of a predetermined brief delay, the supply voltage is restored again by monoflops (MFSK) respectively provided in a plurality or all groups of active circuit components of an electronic circuit.